WCP52: Weekly Status Report

Week of 2014-10-17

Harrison Owens

Weekly Hours: 1 hour meeting with faculty adviser

20 mins looking over final PRS

1 hour researching suitable Op-Amps

Accomplishments

* Started Preliminary research into possible Operational Amplifiers needed for Frequency output circuit
* Started Preliminary research into design ideas for the rest of design

Major Issues

* unable to become familiar with programming the ARM family microcontroller which is necessary for the design
* Unable to produce a concrete plan for frequency output circuit
* Still unfamiliar with KiCad schematic design

Plans

* To become familiar with the ARM microcontroller
* To again become familiar with KiCad

Chris Pavlina

* Hours spent:
  + 4: Circuit design investigation, possibly relevant to specifications
  + 1: Planning
  + 1: Final revision of PRS
* Accomplishments:
  + Started parts research for output amplifier, synthesizer
  + Revised specifications - smaller output amplitude
  + Wrote preliminary design plans
* Problems:
  + Design schedule is only vague
* Plans:
  + Finish a list of milestones and dates by Monday
  + Buy another Atmel SAM4S development board
  + Acquire about samples/eval board for AD9958

Kaidi Xu

Weekly Hours: 1

Accomplishments:

● Started research for output amplifier and synthesizer design

● Started research for input protection and signal switching

● Revised specifications

Major Issues:

● Unable to have a plan for PCB layout

● Should make a specific design schedule for the rest of the project

Plans:

● To make a schedule meeting at next Monday to solve issues

● Finish synthesizer design( AD9958 )

Kenneth Zach

Hours Spent:

* 1 hour FA meeting
* 1.5 hours group meeting
* 20 minutes reviewing PRS document
* 10 minutes updating presentation
* 30 minutes researching how to do phase analysis

Accomplishments

* Learned a little bit about devices that detect zero crossing (possible for use in phase analysis)
* Helped complete PRS document
* Wrote another weekly status report

Issues

* Still need to look into more ways to do phase analysis
* Need high level system block diagram
* Need to schedule ourselves and have group deadlines to get things done
* Learning about ARM, since I’ve never had to before.

Plans

* Meet with group on Monday and get a schedule for, at least, the rest of this semester.
* Begin working on next deadline whose draft is due Friday
* Get through this week of loads of work and begin, over the weekend, playing with a development board in order to learn a little bit about ARM,